

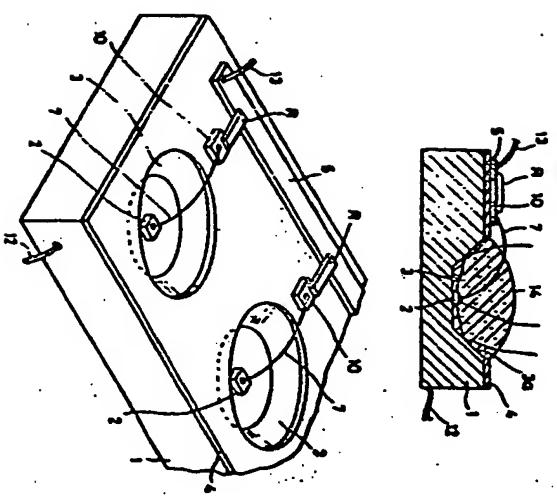
## PATENTS ABSTRACTS OF JAPAN

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(54) ILLUMINATION SYSTEM  
 (11) 62-196878 (A) (43) 31.8.1987 (19) JP  
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**PURPOSE:** To readily manufacture an illuminator by directly arranging a light emitting element in an element containing recess to conduct it with a metal substrate, thereby eliminating the necessity of laminating an insulating layer and a conductive layer in the recess.

**CONSTITUTION:** A plurality of element containing recesses 3 are formed at a suitable interval longitudinally on a metal substrate 1 made of a conductive material, and the surface except the recesses 3 is covered with an insulating layer 4. A common conductive layer 5 is so formed on the layer 4 as to extend longitudinally of the substrate 1. A light emitting element 2 is made of a semiconductor chip, directly disposed fixedly at the center on the bottom of each recess 3 to conduct with the substrate 1, and electrically connected via gold wirings 7 as leads with the conductive layer 10 of the element.



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LEGENDE zu den Bibliographiedaten	(54) Titel der Patentanmeldung	(22) Anmeldetag in Japan
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	(21) Aktenzeichen der JP-Anmeldung	(72) Erfinder
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